

**STABILIZATION TECHNIQUE FOR HIGH REPETITION RATE GAS
DISCHARGE LASERS**

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RELATED APPLICATION

This application claims priority under 35 USC §119 to provisional application no. 60/198,058 entitled "Stabilization Technique for the High Repetition Rates Gas Discharge Lasers" filed on April 18, 2000.

BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention relates to laser systems. In particular, the present invention relates to implementing active loads in the discharge circuitry design of gas discharge lasers to provide stabilization of high repetition rate gas discharge laser systems.

2. Description of the Related Art

Pulse gas discharge lasers, emitting in the deep ultraviolet (DUV) and vacuum ultraviolet (VUV) region are widely used in various industrial applications such as microlithography, photoablation, and micro-machining, among others. For microlithographic applications, currently used systems include line narrowed excimer lasers, such as ArF (193 nm) lasers and KrF (248 nm) lasers, as well as molecular fluorine (F₂) lasers emitting at 157 nm, which are efficient and exhibit high energy stability at high repetition rates, for

example, at 1-2 KHz or more. Figure 1 illustrates a schematic arrangement of a pulsed gas discharge electrical circuit of a typical gas discharge laser system. As shown, a pair of discharge electrodes 101, 102 is coupled to a discharge circuit which includes a peaking capacitance C_p and an inherent inductance L_d between the peaking capacitance C_p and the discharge electrode 101. Such discharge electrical circuitry may be found in current gas discharge lasers such as excimer lasers and molecular fluorine laser systems.

Referring to Figure 1, the area between the discharge electrodes 101, 102 defines a region referred to as a gas discharge region 103. As can be seen, the pair of elongated discharge electrodes 101, 102 of the gas discharge laser, one of which (in this case, the discharge electrode 102) may be connected to a ground or reference potential, are separated by the gas discharge region 103 which is filled with a high pressure laser gas. Moreover, the discharge electrode 101 is connected to the output of the high voltage pulsed generator which is capable of providing fast and powerful charging of the peaking capacitor C_p up to the electrical breakdown voltage of the gas discharge gap. Powerful pulsed source(s) or preionization units for providing ultraviolet light in a spark or corona discharge provide an initial preionization of the gas mixture in the discharge region, and are typically positioned in the vicinity of the discharge region 103 between the discharge electrodes 101, 102. The preionizer(s) provide an initial ionization, or preionization, of the laser gas during the charging of the peaking capacitance C_p which receives an electrical pulse initially provided by the charging of a main storage capacitor by a high voltage (HV) power supply when the main storage capacitor is discharged through a switch such as a thyatron or a solid state switch.

Referring back to Figure 1, in such gas laser systems the HV electrical circuitry which is used for the excitation of the gas discharge in the pulsed gas laser systems may be schematically sub-divided into two parts. The first part of the HV electrical circuitry may include the peaking capacitance C_p which is configured to store electrical energy, and used during the gas breakdown phase.

5 The second part of the HV electrical circuitry may include the HV pulsed power generator which is used for the fast and efficient charging of the peaking capacitor C_p up to the breakdown voltage of the gas. In particular, the HV pulsed power generator may include a suitable HV pulsed device such as a gas filled thyatron, or a solid state switch such as a thyristor or an IGBT).

Additional information may be found in R. S. Taylor, K.E. Leopold, Applied Physics, B59 (1994) 479; U.S. Patents No. 6,020,723, 6,005,880, 5,729,562, 5,914,974, 5,936,988, 6,198,761, 5,940,421, and 5,982,800, and pending U.S. patent applications no. 09/649,595, and 09/453,670, the
10 disclosures of each of which are expressly incorporated herein by reference for all purposes.

A problem encountered with typical pulsed electrical gas discharges in strongly electronegative gas mixtures (i.e., containing a halogen component) at elevated pressures (for example, several bars) is a certain degree of instability.
15 The short phase of the uniform glow discharge, usually less than 100 nanoseconds, corresponding to the pumping of the laser medium, is typically terminated by rapidly developing streamers. The streamers themselves are temporally inconsistent which leads to the discharge instabilities. In addition, the existence of streamers at the ends of discharges produces excessive wear on
20 the electrodes. In view of these problems caused by streamers, it is desired to suppress them. It is therefore desired to have a gas discharge laser including a discharge circuit wherein the main input of the energy into the gas discharge is quickly realized, or that provides very short, intense electrical pulses to the main discharge load, and is terminated without extended and inconsistent
25 streamers reducing the discharge stability from pulse to pulse and without excessively wearing the discharge electrodes.

SUMMARY OF THE INVENTION

30 In view of the foregoing, a discharge circuit for a pulsed gas laser system in accordance with one embodiment of the present invention includes a

pair of spaced-apart electrodes defining a discharge region as a main load, a capacitance coupled to one of the pair of electrodes for providing electrical pulses to the electrodes, and an additional load electrically coupled between the capacitance C_p and one of the discharge electrodes.

5 The additional load may include one or more resistors, a resistor array, a resistor or resistor array coupled with a variable inductance and/or a saturable inductance, or another dissipative electrical component for dissipating electrically energy between the main load and the capacitance to facilitate termination of electrical discharges between the electrodes and in turn suppress the formation or influence of streamers. The additional load may be coupled in series or in parallel with the capacitance and the main load, and a portion of the additional load may be coupled in series and a portion may be coupled in parallel with the capacitance and the main load. Any series connected portion of the additional load may be coupled to a high voltage or grounded main electrode. The resistor or resistors may have a value comparable to a wave impedance of the discharge circuit. Alternatively, the resistor or resistors may have a value comparable to an active impedance of the gas discharge during a maximum discharge current phase. The additional load is preferably a passive resistance, and may alternatively have an active feature such as a voltage dependence or a temperature dependence.

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The circuit may further include a cooling unit, wherein the additional load is provided in the cooling unit. The cooling unit may be provided within a pulsed power module of a laser system which contains electrical components of the discharge circuit particularly susceptible to heating effects. The cooling unit may include one of an air fan and an encapsulated volume with circulating isolating fluid.

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One or more preionization units are preferably also provided for ionizing the laser gas within the discharge region between the pair of main electrodes during the charging of the capacitance just prior to discharging through the electrodes.

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The capacitance may include a series of peaking capacitors, and may include a series of sustaining capacitors. The sustaining capacitors would be coupled to the electrodes by a different inductance than the peaking capacitors, and would be otherwise preferably coupled within the discharge circuit

5 similarly to the peaking capacitors defining the capacitance coupled to the main electrodes. The pair of electrodes, the capacitance and the additional load may form a series configuration such as an electrical loop, or the additional load may be coupled to the electrodes in parallel with the capacitance. The circuit further includes a power generator configured to provide power to the capacitance for

10 charging the capacitance. The power generator may include a high voltage pulsed power generator. The power generator is preferably connected to a main storage capacitor which is charged during a charging cycle. A solid state switch is used for discharging the main storage capacitor to the rest of the discharge circuit which preferably includes one or more pulse compression stages before

15 the peaking capacitance connected to the main load. A transformer and/or voltage doubling circuit may also be coupled between the main storage capacitor and the pulse compression stages. A processor is connected in a pulse energy or energy dose control feedback loop with a detector for providing charging voltage values to which the power supply charges the main storage

20 capacitor between discharges of electrical pulses.

The circuit may further include a ground terminal coupled to the capacitance.

A discharge circuit in accordance with another embodiment of the present invention includes a pair of discharge electrodes, an area between the

25 pair of electrodes defining a gas discharge area, a peaking capacitance coupled to one of the pair of discharge electrodes, the peaking capacitance configured to store charge, an additional load including a resistor, resistor array for low inductivity, or a resistance coupled with a saturable or variable inductance is coupled between the discharge electrodes and the peaking capacitance, either in

30 series or in parallel, and a ground or reference voltage terminal is further

preferably coupled to the other terminal of the peaking capacitance and the main load or discharge electrodes, where the pair of discharge electrodes, the peaking capacitor and the resistor form an electrical loop. That is, a first electrode of the pair of main electrodes is preferably coupled to the peaking capacitance, wherein the additional load is coupled either in series between the peaking capacitance and the first main electrode, and a second main electrode is connected to a ground or reference voltage along with a ground or reference terminal of the peaking capacitance. When the additional load is connected in parallel with the peaking capacitance, the additional load is also preferably connected to the ground or reference voltage.

The circuit may further include a cooling unit for cooling the additional load or resistance.

The gas discharge area may be filled with a high pressure laser gas, e.g., at 2-7 bar, and preferably around 3-5 bar. The gas mixture may include molecular fluorine and an active rare gas such as krypton or argon, of a KrF or ArF laser, respectively, while the gas mixture may be pressurized with a buffer gas of neon and/or helium. The laser active gas may solely include molecular fluorine such as for a molecular fluorine laser, wherein the buffer gas may include neon and/or helium or a combination thereof.

A resistor or a resistor array may be connected between the peaking capacitor and high voltage main electrode, while the other discharge electrode is connected to ground or a reference voltage.

A discharge circuit for use in a laser system in accordance with yet another embodiment of the present invention includes a pair of discharge electrodes, wherein an area between the pair of electrodes defines a gas discharge area. A first peaking capacitance is coupled directly to the pair of electrodes. A second peaking capacitance is also coupled to the pair of electrodes, wherein a resistance or an otherwise additional load is coupled, either in series or in parallel, between the second peaking capacitance and the discharge electrodes. A ground or reference terminal is preferably coupled to

the first and second peaking capacitors and a ground electrode of the pair of main discharge electrodes, wherein the pair of discharge electrodes, the second peaking capacitances and the resistance or additional load may form a series electrical loop or parallel electrical combination, while the first peaking capacitance and discharge electrodes form another electrical loop. The additional load may include a resistor, a resistor array, a resistor combined with a variable or saturable or saturable inductance, or other means for dissipating electrical energy between the discharge electrodes and the second peaking capacitance.

10 The circuit may include a cooling unit for cooling the resistor.

 The circuit may include a high voltage pulsed generator to provide power to the first and second peaking capacitors, and the gas discharge area may include a high pressure laser gas.

15 A method of providing a discharge circuit for a pulsed gas laser system in accordance with still a further embodiment of the present invention includes providing a pair of electrodes, coupling a capacitance to a first electrode of the pair of electrodes, the capacitance configured to store charge, and coupling an additional load between the capacitance and the first electrode.

20 A further method includes charging a main storage capacitor of a pulsed gas discharge excitation laser system, discharging the main storage capacitor through a pulse compression circuit to a peaking capacitance as an electrical pulse, and dissipating the energy of the electrical pulse through a main load and an additional load, the main load including a discharge region filled with a gas mixture and the additional load including a resistor, resistor array or a resistance coupled with a variable or saturable inductance, and wherein the additional load is coupled either in series between the peaking capacitance and the main load or in parallel with the peaking capacitance.

25 According to either of the above methods, the load may include a resistor, and the resistor may have a value comparable to a wave impedance of said discharge circuit, or a value comparable to an active impedance of the gas

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discharge during a maximum discharge current phase.

The method may further include the step of providing cooling the additional load, where the step of cooling may include the step of providing either an air fan or an encapsulated volume with circulating oil.

5 The method may further include the step of defining a volume between the pair of electrodes as a gas discharge area having a width that allows a clearing ratio of the laser gas to be sufficient in view of the repetition rate of the laser, which may be 2 kHz, 4 kHz or more.

10 The method may further include the step of providing ionization of a laser gas in the gas discharge area during the charging of the capacitance.

The pair of electrodes, the capacitance and the load may form an electrical loop.

15 A method of providing a discharge circuit in accordance with yet still another embodiment of the present invention includes defining an area between a pair of discharge electrodes as a gas discharge area, coupling a peaking capacitance to the pair of discharge electrodes, coupling an additional load between one of the discharge electrodes and the peaking capacitance, and coupling a ground or reference voltage terminal to one of the discharge
20 electrodes which couples either directly to a ground terminal of the peaking capacitance or to the peaking capacitance through the additional load, where the pair of discharge electrodes, the peaking capacitance and the additional load form a series or parallel electrical combination.

25 A method of providing a discharge circuit for use in a laser system in accordance with yet a further embodiment of the present invention includes providing a pair of discharge electrodes, an region between the pair of electrodes defining a gas discharge region, coupling a first peaking capacitance to the pair of electrodes, and coupling a second peaking capacitance to the pair of electrodes, and coupling a resistance or an otherwise additional load between
30 the second peaking capacitance and the discharge electrodes, and coupling one

or the discharge electrodes to ground, along with coupling at least one of the first and second capacitances also to ground, wherein if the additional load is coupled between the second peaking capacitance and the ground discharge electrode, then the second capacitance is coupled to ground through the additional load, and if the additional load is coupled to the high voltage discharge electrode, the the second capacitance is coupled directly to ground, and the first capacitance is coupled directly to ground in either case.

These and other features and advantages of the present invention will be understood upon consideration of the following detailed description of the invention and the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 illustrates a schematic arrangement of a typical discharge circuit of a gas discharge laser.

Figure 2a illustrates a schematic arrangement of a discharge circuit of a gas discharge laser discharge in accordance with a first embodiment of the present invention.

Figure 2b illustrates a schematic arrangement of an overall discharge circuit of a gas discharge laser in accordance with a preferred embodiment.

Figure 3 illustrates a schematic arrangement of a discharge circuit of a gas discharge laser in accordance with a second embodiment of the present invention.

Figure 4 illustrates a schematic arrangement of a discharge circuit of a gas discharge laser in accordance with a third embodiment of the present invention.

Figure 5 illustrates a schematic arrangement of a discharge circuit of a gas discharge laser in accordance with a fourth embodiment of the present invention.

Figure 6 illustrates a schematic arrangement of a discharge circuit of a gas discharge laser in accordance with a fifth embodiment of the present

invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

5 Figure 2a illustrates a schematic arrangement of a laser discharge electrical circuitry in accordance with a first preferred embodiment. Referring to Figure 2a, there is provided a pair of discharge electrodes 201, 202 coupled to a discharge circuit which includes a peaking capacitor C_p and an inherent inductance L_d therebetween. The discharge electrical circuitry may be used as
10 part of the overall discharge circuitry of a gas discharge laser such as of an excimer or molecular fluorine laser. For this purpose, U.S. patents no. 6,020,723, 6,160,832, 6,212,214, 6,157,662, 6,154,470 and 6,005,880 and U.S. patent applications no. 09/657,396, 09/247,887, 09/447,882, 09/594,892, 09/418,052, 09/452,353, 09/532,276, 09/453,670, 09/602,184, 09/599,130,
15 09/598,552, 09/574,921, 09/640,595, 09/629,256, 09/692,265, 09/734,459, 09/715,803, 09/738,849, 09/688,561, 09/718,809, 09/771,013, 09/780,124, 60/204,095, 09/780,120, 09/780,120, 60/200,163, 09/584,420, which are assigned to the same assignee as the present application, are hereby incorporated by reference as describing preferred and alternative embodiments
20 of overall discharge circuits and gas discharge laser systems. United States patent no. 6,020,723 particularly shows an overall discharge circuit of an excimer or molecular fluorine laser, and more particularly Figure 2 therein (and which is described in the '723 patent wherein that description is not repeated here) which is shown at Figure 2b herein, according to a preferred embodiment,
25 wherein the circuit shown at Figure 2b herein is preferably modified according to the any the preferred embodiments set forth below and schematically illustrates at Figures 2a-6.

 Referring back to Figure 2a, the region between the discharge electrodes 201, 202 is defined as a gas discharge region 203. Moreover, a resistor R is
30 shown provided between the peaking capacitor C_p and the discharge electrode

201. As discussed above, the peaking capacitor C_p is configured to store electrical energy, and is directly used during the phase of the gas breakdown.

The HV pulsed power generator of the HV electrical circuitry is used for the fast and efficient charging of the peaking capacitor C_p until the breakdown voltage of the gas. In particular, the HV pulsed power generator may include any suitable HV pulsed device such as including a gas filled thyatron, or a solid state switch (for example, a thyristor or an IGBT-based switch), for discharging a main storage capacitor through one or more pulse compression stages to the peaking capacitance. A transformer is also preferably included between the main storage capacitor and the pulse compression circuit. The main storage capacitor is charged by a high voltage power supply which receives signals indicative of charging voltages to be applied to the discharge electrodes from a processor which monitors pulses energies and/or moving average pulse energies or energy doses in a feedback loop with the power supply circuit.

As can be seen from Figure 2a, the resistor R is connected in series between the discharge electrode 201 and the peaking capacitor C_p . In this manner, in accordance with one embodiment of the present invention, the resistor R functions as an additional load in the electrical circuit of the gas discharge electrical loop.

In the manner described above, in accordance with one aspect of the present invention, an additional load such as resistors or a resistor array may be implemented into the electrical circuitry of the gas discharge. In addition, the additional load may include a variable or saturable inductor or other dissipative electrical component known to those skilled in the art. More particularly, in accordance with one aspect of the present invention, the resistor may be coupled directly in series with the gas discharge. As described below, the additional load may be coupled in parallel to the peaking capacitance, and may be partly coupled in series and partly coupled in parallel with the peaking capacitance. The value of the resistor may be comparable to the active impedance of the gas discharge during the main phase of the dissipation of the

energy in the gas discharge. Furthermore, the resistance value may be comparable to the wave impedance of the gas discharge electrical loop, and in particular, may be less or greater than the wave impedance.

Figure 3 illustrates a schematic arrangement of a laser discharge electrical circuitry in accordance with a second preferred embodiment. Referring to Figure 3, there is provided a pair of discharge electrodes 301, 302, the region between which defining a gas discharge region 303. Further shown in Figure 3 is inductance L_d which may be inherent in the circuitry, and a peaking capacitor C_p coupled to the discharge electrode 301. Moreover, a resistor R is coupled between the discharge electrode 302 and the peaking capacitor C_p . In contrast to the first preferred embodiment described above, the resistor R is coupled between the peaking capacitance and a ground or reference discharge electrode 302, rather than to the high voltage electrode 301.

As can be further seen from Figure 3, power from the HV pulsed power generator is applied to the discharge electrode 301, which, as discussed above, is used for charging the peaking capacitor C_p until the breakdown voltage of the gas. In contrast, as discussed above, the power from the HV pulsed power generator in the embodiment shown in Figure 2 is applied at a node between the resistor R and the peaking capacitor C_p .

Indeed, as can be seen from Figures 2 and 3, in accordance with the embodiments of the present invention, a resistor R is placed in series between the peaking capacitors C_p and at least one of the main discharge electrodes (electrode 201 in Figure 2, and electrode 302 in Figure 3) of the gas discharge laser (such as, for example, an excimer laser or a molecular fluorine laser) to add additional loads between the peaking capacitor C_p and the at least one of the discharge electrodes. In one embodiment, the resistor R may be replaced by other circuit elements, e.g., CuSO_4 solutions which adds additional active loads between the peaking capacitor C_p and the at least one of the discharge electrodes.

Moreover, while the heat dissipation in the resistor(s) R may be

significant, some precaution for cooling the resistor(s) R may be used. In particular, the cooling element may include an air fan or an encapsulated volume (box) with circulating oil or other insulating fluid (provided for insulation). More specifically, in the latter case of the insulating fluid, the fluid could be cooled by means of a heat exchanger with cold line water.

5 Additionally, the encapsulated box in one aspect may be fabricated with dielectric or metal with feedthroughs for the connection of the resistor(s) R to the element of the gas discharge loop.

In addition, it should be noted that the placement of the resistor(s) R in the laser discharge circuit may require some efforts in cooling. To this end, additional equipment for cooling the resistor(s) R may be incorporated into the laser arrangement. Furthermore, the resistor(s) R in one aspect may be implemented into the laser pulsed power module which itself requires a cooling unit. In this approach, additional cooling unit may be unnecessary. Moreover,

10 in the case where the resistor(s) R are integrated into the laser pulsed power module, the laser pulsed power module may have distributed multiple outputs rather than a single output, connected directly to the laser discharge electrodes or to the peaking capacitor Cp arrangement around the laser discharge electrodes (laser discharge chamber).

Figure 4 illustrates a schematic arrangement of a laser discharge electrical circuitry in accordance with yet another embodiment of the present invention. Referring to Figure 4, there is provided a pair of discharge electrodes 401, 402, with the area between the two electrodes 401, 402 defining a gas discharge area 403. Further shown in Figure 4 are inductances Ld1 and Ld2,

15 which may be inherent to the circuitry shown and differ in value preferably due to there being different lengths of connecting conductors between the first and second peaking capacitors and the electrodes 401, first and second peaking capacitors Cp1 and Cp2 respectively coupled to the discharge electrode 402. Additionally, a resistor R is provided between the discharge electrode 401 and

20 the second peaking capacitor Cp2, wherein the resistor or otherwise additional

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load may be connected between either peaking capacitance Cp1 or Cp2 and the electrode 402, or some or all of the additional load may be connected in parallel with the capacitance Cp1 and/or Cp2. Finally, the power from the HV pulsed power generator is applied as shown at a node between the first peaking capacitor Cp1 and the discharge electrode 401, and at a node between the
5 second peaking capacitor Cp2 and the resistor R.

As can be seen, the discharge circuit shown in Figure 4 may be configured to act similar to a spiker-sustainer discharge circuit, or may be otherwise configured as set forth at U.S. patent application no. 09/640,595,
10 which is assigned to the same assignee as the present application and is hereby incorporated by reference. In particular, the first peaking capacitor Cp1 is positioned as close as possible to the electrode 401 and thus the gas discharge chamber to provide fast electrical pumping to near the steady state discharge level. The second peaking capacitor Cp2 is connected to the electrodes 401,
15 402 via the resistor R. The resistor R stabilizes the current through the discharge during the steady state discharge. If the peaking capacitors Cp1, Cp2 are positioned on the opposite sides of the discharge electrodes as shown in Figure 4, the discharge itself divides the entire peaking capacity in a "spiker-part" (i.e., Cp1 and a "sustainer-part" (Cp2)). In this case, there may be no need
20 for additional saturable inductivities. Furthermore, it should be noted that the approach shown in Figure 4 is not limited to the discharge circuit shown in Figure 4, but may be applied to similar circuits for pulsed discharges, and further, also for values of the resistors less than or equal to 0.01 Ohms.

In the manner described above, in accordance with one aspect of the
25 present invention, additional load such as resistors may be implemented into the electrical circuitry of the gas discharge. More particularly, in accordance with one aspect of the present invention, the resistor may be coupled in series with the gas discharge. The value of the resistor may be comparable to the active impedance of the gas discharge during the main phase of the dissipation
30 of the energy in the gas discharge. Furthermore, the resistance value may be

comparable to the wave impedance of the gas discharge electrical loop, and in particular, in principle, may be less or greater than the wave impedance.

As an example, resistors of type KOAOHM HPC5-K 22 Ohm which is 44 mm in length and 8mm in diameter, is connected in parallel from 10 to 40 pieces. The resulting data is shown below in Table 1 which was measured with KrF laser.

Table 1.

Resistance – Ohm	1.83333	1.1	0.6875
Estor, J (stored energy)	2.888	2.888	2.888
Eres, J (dissipated energy)	0.91281	0.78639	0.85369
Eres, % from Estor (dissipated energy)	31.6069	27.2295	29.5599
(Estor-Eres), J	1.97519	2.10161	2.03431
(Estor-Eres), % from Estor	68.3931	72.7705	70.4401
Eout, mJ (laser output energy)	30.6	35	38.1
Eff, tot, %	1.05956	1.21191	1.31925
Eff, -res, %	1.54922	1.66539	1.87287
Eout0, mJ without resistors	45	45	45
Eout, % from Eout0	68	77.7778	84.6667
Eff, % without resistors	1.55817	1.55817	1.55817

where total percentage of efficiency (Eff, tot, %) indicates the efficiency of the laser emission (broadband), determined by taking into account the whole energy (Estor) stored in the storage capacitor of the HV pulsed generator, and further, where the total percentage of efficiency with the resistor elements (Eff, -res, %) indicates the efficiency of the laser emission (broadband). Advantageous features particularly resulting from the values shown at Table 1 can be summarized: For a resistor with $R=0.69\ \Omega$

- The dissipated energy in the discharge region is reduced by 29.6%

- The efficiency (output energy/energy stored in the discharge volume) has be increased by 19.8% in comparison to the case of no additional load.

5 Implementing additional resistors in the gas discharge electrical loop may reduce the laser output energy. However, the decrease in the laser output energy is not proportional to the decrease of the energy dissipated in the gas discharge which takes place due to the simultaneous dissipation of the energy in the resistors. More specifically, the laser efficiency determined from the power
10 dissipated in the gas discharge may be increased, which, in turn, indicates that part of the losses in this case could be shifted to the resistors placed outside of the laser tube. This is significant for laser operation at the high repetition rates, while allowing a reduction of the thermal load of the laser tube (i.e., the laser electrodes and laser gas). Moreover, the issue of cooling the laser gas inside the laser tube could be in part, replaced by the cooling of the resistor(s) placed
15 outside of the laser tube. This may also have a positive impact upon the blowing of the laser gas between the discharge electrodes. In other words, while the stress of the discharge electrodes and gas may be less, the necessary gas velocity could be reduced.

Even in the case where the value of the resistance is relatively small
20 (resulting in low dissipated power in the resistors), their role in the gas discharge circuitry is significant, since the resistor is configured to terminate the gas discharge at the latest stages of microarcing and streamers when the gas discharge active impedance is low and the wearing of the laser electrodes and gas is maximized.

25 Furthermore, in one aspect, the resistors of the gas discharge circuitry may make the gas discharge more soft and uniform, thus improving the laser output pulse-to-pulse energy stability. Moreover, with the various aspects of the present invention, the performance of the laser system at a high repetition rates may be improved, thus improving the lifetime of the laser tube and laser
30 gas.

As discussed above, the peaking capacitors may be equivalent and be positioned as close as possible to the gas discharge chamber to provide fast and powerful electrical pumping of the laser active volume which is designed for the excimer or molecular fluorine laser systems. Furthermore, the peaking capacitors may be further subdivided into several groups of capacitors with different connection inductivities to the gas discharge electrodes, and may have different HV power generators, or operate as the transforming lines. In this regard, it should be noted that the HV electrical circuitry may include additional elements and sub-circuits connected between the main gas discharge electrodes for correction of the waveform of the high voltage pulse applied between the electrodes.

In this manner, one aspect of the present invention is directed to implementing additional loads (e.g. resistors or resistor arrays or resistors or resistor array coupled with one or more variable or saturable inductors) in the peaking circuit and in particular coupled in series with the discharge. The value of the resistor may be comparable to the active impedance of the gas discharge during the main phase of the dissipation of the energy in the gas discharge. Furthermore, the resistance value may be comparable to the wave impedance of the gas discharge electrical loop, and in particular, in principle, may be less or greater than the wave impedance.

Figure 5 schematically illustrates a fourth preferred embodiment. The fourth embodiment is similar to that described above and shown at Figure 2. The difference between the fourth and first embodiments is that a variable or saturable inductance L' is inserted between the peaking capacitance and the discharge electrode 501. A variable or saturable inductance L' may be similarly inserted into the circuits shown and described with respect to either of the second or third embodiments of Figures 3 or 4, as well.

Figure 6 schematically illustrates a fifth preferred embodiment. The general feature of the fifth embodiment of Figure 6 is that the additional load is inserted in parallel with the peaking capacitance C_p and the electrodes 601, 602.

The circuit of the fifth embodiment may or may not include the variable or saturable inductance L' in series with the resistance R , i.e., inclusion of the variable or saturable inductance is optional. In addition, any of the preferred embodiments described above and shown at Figures 1-5 may be modified such that all or a part of the additional load is coupled in parallel with the peaking capacitance C_p , C_{p1} and/or C_{p2} . Alternatively, the variable or saturable inductance L' may be coupled in series with the peaking capacitance and either electrode 601, 602 while the resistance R is coupled in parallel, as shown in Figure 6, and the resistance R may be coupled in series with either of the electrodes 601, 602 while the variable or saturable inductance L' is coupled in parallel, as shown in Figure 6.

Various other modifications and alterations in the structure and method of operation of this invention will be apparent to those skilled in the art without departing from the scope and spirit of the invention. Although the invention has been described in connection with specific preferred embodiments, it should be understood that the invention as claimed should not be unduly limited to such specific embodiments. It is intended that the following claims define the scope of the present invention and that structures and methods within the scope of these claims and their equivalents be covered thereby.